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CYCLIC TRANSIENTS IN THE CIRCUITS OF ELECTRIC DISCHARGE INSTALLATIONS TAKING INTO ACCOUNT THE INFLUENCE OF MAGNITUDE AND RATE OF DISCHARGE CURRENTS RISE ON RESISTANCE OF ELECTRIC SPARK LOAD

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Abstract

The cyclic transients in the charge-discharge circuits of capacitor of semiconductor electrical discharge installation using the mathematical model of resistance of the electrical spark load, which depends on magnitude of discharge current, rate of its change and duration of the time pause between discharge pulses are studied. The mathematical model reflects the U-shaped character of the change in the resistance of such load in time (in particular, in the case of volumetric electro-spark dispersion of a layer of current-conductive granules in a dielectric

liquid). Also this model takes into account that the load resistance can have different values at the beginning and end of the discharge process of the capacitor and varies during the pause between the discharge pulses. A comparison of the transient results (discharge pulse duration, released in the load energy, average pulse power and rate of current rise in the load) in the discharge circuits with a nonlinear resistance of spark load and the energy-equivalent linear resistance is made. The analysis of ways to improve the dynamic characteristics of pulse currents in the load in the case of control of capacitor discharge duration is carried out. References 17, figures 4, tables 2.

Key words: nonlinear resistance, electric spark load, pulse, transients, mathematical model.

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