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COMPUTER SIMULATION OF HIGH-VOLTAGE DAC

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Abstract

A computer model of a high-voltage digital-to-analog converter (HVDAC) for use in portable metrological installations is proposed. The principle of operation is described, the scheme of the model, the output signals are shown, the influence of the discreteness of the HVDAC and the resistance of the access keys on the quality of the output signal is investigated. References 9, figures 3, table 1.

Key words: reproduction, metrological provision, electricity, power amplifiers, voltage digital-analog conversion, computer simulation.

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