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SYNTHESIS OF THREE-LOOP CIRCUITS OF SEMICONDUCTOR ELECTRIC DISCHARGE INSTALLATIONS WITH RESERVOIR CAPACITOR

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Abstract

It is shown that if in the three-loop circuits of the capacitors of electric discharge installations (EDI) the charge thyristor is switched on after the end of the previous reverse recharge of the capacitor to a positive voltage, then in the EDI is realized a negative voltage feedback and for the capacitor charge voltage will be satisfied the inequality $UC \leq 2U_{GDV}$. *If the thyristor* VT_1 *is switched on before the end of the capacitor reverse recharge, it is possible to realize a controllable voltage feedback, which can be either negative or positive, depending on the voltage polarity on the capacitor at the moment of VT*

1

activation.

References 14, figures 4.

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