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MATHEMATICAL MODEL OF HIGH VOLTAGE DAC FOR ESTIMATION OF DISTORTION OF THE OUTPUT SINUSOIDAL SIGNAL

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Abstract

Two simplified mathematical models for calculate the change in the output impedance and distortion sine wave output of the high-DAC for different types of switching reference sources are proposed. The advantages of the circuit with switching transistors by help of the mathematical modeling is provided. References 7, figures 8.

Key words: reproduction, metrological equipment, electricity, high voltage digital-analog converter.

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